

In the Claims:

36. (Amended) The method for accessing [a] an asynchronously-accessible dynamic random access memory, comprising the steps of:

receiving an external row address to the asynchronously-accessible dynamic random access memory;

selecting between a burst mode and a pipelined mode of operation of the asynchronously-accessible dynamic random access memory;

selecting between a read operation and a write operation of the asynchronously-accessible dynamic random access memory; and

obtaining a first external column address for accessing the asynchronously-accessible dynamic random access memory.

37. (Amended) [A] The method, as in Claim 36, further comprising [a] the step of:

obtaining a second external column address subsequent to the first external column address for operation in the pipelined mode.

38. (Amended) [A] The method, as in Claim 36, further comprising [a] the step of:

generating an internal column address subsequent to the first external column address for operation in the burst mode, the internal column address [being] patterned after the first external column address.

39. (Amended) [A] The method, as in Claim 36, further comprising [a] the step of:

selecting at least one address pathway based on the selection [as] between the burst mode and the pipelined mode.